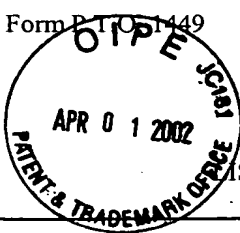


Form PTO-1449 <div style="text-align: center;">  <p>U.S. Department of Commerce Patent and Trademark Office</p> </div>					Docket No. P01,0365		Serial No. 10/040,395			
LIST OF PRIOR ART CITED BY APPLICANT (use several sheets if necessary)					Applicant Yue et al		Filing Date January 7, 2002		Group Art Unit	
U.S. PATENT DOCUMENTS										
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)										
<i>SAR</i>	AR	Analysis and Optimization of Accumulation-Mode Varactor for RF ICs - Soorapanth et al 1998 Symposium on VLSI Circuits Digest of Technical Papers, pp. 32-33, June 1998								
<i>SAR</i>	AS	UTSi CMOS: A Complete RF SOI Solution - Analog Integrated Circuits and Signal Processing, 25, 171-179, 2000 Kluwer Academic Publishers								
<i>SAR</i>	AT	Design of High-Q Varactors for Low-Power Wireless Applications Using a Standard CMOS Process - Porret et al - IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000								
<i>SAR</i>	AU	A Metal-Oxide-Semiconductor Varactor - Svelto et al - IEEE Electron Device Letters, Vol. 20, No. 4 April 1999								
	AV									
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